UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/753,281	12/29/2000	Christophe Joly	50325-0506 (2853)	7118

				EXAMINER
29989	7590	02/22/2006		SONG, HOSUK

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ART UNIT	PAPER NUMBER
2135	

DATE MAILED: 02/22/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 10/03)

	Office Action Summary	**Application No.** 09/753,281	**Applicant(s)** JOLY ET AL.
		Examiner HOSUK SONG	**Art Unit** 2135

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE _3_ MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on _29 December 2000_.

2a)☐ This action is **FINAL**.   2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) _1-27_ is/are pending in the application.

    4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) _1,2,4,5,7-18,21,22 and 27_ is/are rejected.

7)☒ Claim(s) _3,6,19,20 and 23-26_ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a)☐ All   b)☐ Some * c)☐ None of:

      1.☐ Certified copies of the priority documents have been received.

      2.☐ Certified copies of the priority documents have been received in Application No. _____.

      3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

    * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1)☒ Notice of References Cited (PTO-892)

2)☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3)☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
    Paper No(s)/Mail Date _____.

4)☐ Interview Summary (PTO-413)
    Paper No(s)/Mail Date. _____ .

5)☐ Notice of Informal Patent Application (PTO-152)

6)☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis

for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

1.   Claims 1-2,4-5,7-18,21-22,27 are rejected under 35 U.S.C. 102(b) as being anticipated by Bright

et al(US 5,729,559).

Claims 1-2,17-18: Bright disclose a first plurality of processing stages connected in series and

having a feedback channel connecting a last stage of the first plurality to one of the processing stages

among the first plurality of processing stages wherein each processing stage of the first plurality is

configured to process one block of data from a data stream during one processing cycle in (fig.2 and

col.2,lines 43-54;col.4,lines 54-65). Bright disclose a parallel input queue comprising a second plurality

of input queues connected in parallel to the first stage and configured to direct a data block to the first

stage alternately from each of a third plurality of data streams in (fig.2 and col.5,lines 1-15;col.8,lines 38-

44).

Claim 4: Bright disclose a second plurality of context registers connected in parallel to the first

stage for directing context information from a context register to the first stage during a processing cycle

with an associated data stream on a corresponding input queue in (col.5,lines 1-15).

Claim 5: Bright disclose first plurality of processing stages implement an encryption process and

the context information associated with the data stream comprises an encryption key for the data stream

in(col.5,lines 4-10).

Claim 7: Bright disclose a parallel output queue comprising a second plurality of output queues

connected in parallel to the last stage for directing a block from the last stage alternately to each of a third

plurality of output streams each output stream corresponding to a respective data stream of the third plurality of data streams in (fig.2).

Claims 8,10-11: Bright disclose one data block from one data stream can be processed at one processing stage during one processing cycle and each interior input block of each data stream is not directed to the first processing stage before an output block based on a previous input data block from the data stream is output from the first plurality of processing stages in (fig.2 and col.5,lines 1-15). Bright disclose the previous input data block precedes the interior input block in the data stream by a delay number of block less than a number of processing stages in the first plurality and the interior block follows the delay number of blocks counting from a beginning of the data stream in (fig.2).

Claim 9: Bright disclose a number of data streams in the second plurality is equal to the number of processing stages in the first plurality in (fig.1).

Claim 12: Bright disclose first plurality of processing stages implement an encryption process and the context information associated with the data stream comprises an encryption key for the data stream in(col.5,lines 4-10).

Claims 13-15: Bright disclose directing alternately to a first processing stage of a first plurality of processing stages connecting in series a second plurality of data streams wherein one data block from one data stream can be processed at one processing stage during one processing cycle each interior input block of each data stream is not directed to the first processing stage before an output block based on a previous input data block from the data stream is output from the first plurality of processing stages in (fig.2 and col.2,lines 43-54;col.4,lines 54-65). Bright disclose the previous input data block precedes the interior input block in the data stream by a delay number of blocks less than a number of processing stages in the first plurality and the interior block follows the delay number of blocks counting from a beginning of the data stream in (fig.2).

Claim 16: Bright disclose identifying a number of stages and a feedback delay number of blocks for the procedure;connecting the stages in series and forming a feedback channel connecting a last stage to a first stage in (fig.2). Bright disclose determining whether the delay number is less than the number of stages;if it is determined the delay number is less, then determining a number of input queues based on the number of stages and the delay number and forming a parallel input queue comprising the number of parallel input queues connected in parallel to the first stage in (fig.2 and col.2,lines 43-54;col.4,lines 54-65).

Claims 21,27: Bright disclose creating and storing a first plurality of processing stages that are connected in series and having a feedback channel connecting a last stage of the first plurality to one of the processing stages among the first plurality of processing stages and creating and storing a parallel input queue comprising a second plurality of input queues connected in parallel to the first stage in (fig.2;col.8,lines 41-43). Bright disclose processing one block of data from a data stream during omne processing cycle using each processing stage of the first plurality and directing a data block to the first stage alternately from each of a third plurality of data streams in ((fig.2 and col.2,lines 43-54;col.4,lines 54-65).

Allowable Subject Matter

2.   Claims 3,6,19-20,23-26 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Information Disclosure Statement

3.   The information disclosure statement filed 4/30/01 fails to comply with 37 CFR 1.98(a)(2), which requires a legible copy of each cited foreign patent document; each non-patent literature publication or

that portion which caused it to be listed; and all other information or that portion which caused it to be listed. It has been placed in the application file, but the information referred to therein has not been considered.

USPTO Contact Information

4.   Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner Song whose telephone number is 5712723857. The examiner can normally be reached on mon-fri.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, KIM VU can be reached on 5712723859. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/ Hosuk Song
Primary Examiner
Art Unit 2135